

CLAIMS:

1. A digital microelectronic circuit comprising a clocked data-processing unit (1) and a converting unit (2) which reads in data present at the output of the data-processing unit, performs a predetermined converting operation on the data and passes on the converted data, characterized in that the converting unit is realized in an asynchronous logic circuit, such that
5 the period of time for performing the converting operation is shorter than the shortest time interval to the next change of the data present at the output of the data-processing unit.

2. A digital microelectronic circuit as claimed in claim 1, characterized in that the converting operation is an encryption, a compression, an error correction, a hash function
10 and/or checking operation, preferably a CRC method.

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